

**Government of Bihar**  
**Department of Industries**

**Resolution**

**Sub:-BIHAR SEMICONDUCTOR POLICY-2026**

**1. PREAMBLE AND POLICY CONTEXT**

**1.1. Background**

Semiconductors are at the heart of modern technology. They power essential systems in healthcare, transport, communication, defence and space. Currently, countries like Taiwan, South Korea, Japan, China and US dominate the semiconductor industry. These countries have launched national strategies to support domestic chip manufacturing and reduce over-reliance on a single region. India is emerging as an important and trusted partner in this global shift. India's chip market is booming, set to hit USD 100–110 Bn by 2030.

In alignment with these national priorities, the Government of Bihar, under the leadership of the Hon'ble Chief Minister and guided by the vision of "Saat Nischay 3.0 – Third Agenda: Samriddh Udyog, Sashakt Bihar", seeks to position the State as Eastern Tech Hub.

Through this policy the State aims to develop a sustainable, innovative-driven semiconductor ecosystem. This approach is intended to promote long-term economic growth (samriddhi), generate high-quality employment for the youth of Bihar, strengthen the State's industrial base, and ensure balanced regional development in consonance with national and state development objectives.

**1.2. Strategic Sector**

The semiconductor sector is of critical strategic importance to both Bihar and India, as semiconductors constitute the foundational technology underpinning modern digital economies, advanced manufacturing, and national security systems. From telecommunications, data centers, and artificial intelligence to electric mobility, Defense electronics, healthcare devices, and renewable energy systems, semiconductors are integral to every high-growth and high-impact sector. Bihar's entry into the semiconductor value chain represents a structural shift towards high-value, technology-intensive manufacturing, enabling the State to leapfrog stages of industrial development, build long-term technological

capabilities, and integrate with global supply chains. The sector offers significant potential for sustainable employment generation, innovation-led growth, and industrial diversification, while positioning Bihar as an emerging hub in India's electronics and deep-tech landscape.

**Strategic importance For Bihar:**

- Transformation to technology-driven economy
- High-quality employment for educated youth
- Reversal of brain drain
- GDP multiplier effect
- Integration into global technology value chains
- Strategic location for serving NE states and SAARC markets
- High consumer market for electronics goods

**1.3. Policy Alignment**

This policy aligns with:

- Saat Nischay 3.0 - Third Agenda: Samriddh Udyog, Sashakt Bihar
- Startup Bihar
- Bihar Skill Development Mission
- India Semiconductor Mission (ISM) - Central Government
- Make in India - National Manufacturing Initiative
- Atmanirbhar Bharat - Self-Reliance Mission
- Digital India - Technology Transformation Program

**1.4. Policy Duration**

- a. The Bihar Semiconductor Policy shall come into force from the date of its notification by the Government of Bihar.
- b. This Policy shall remain in force from the date of its notification until 5 years, or until it is superseded or substituted by another policy, whichever is earlier. The Government of Bihar reserves the right to amend, modify, or withdraw any or all provisions of this Policy at any time.
- c. Any doubt regarding the interpretation of any term and/or any dispute relating to the implementation or operation of any provision under this Policy shall be referred to the Department of Industries, Government of Bihar, for clarification and resolution. The decision of the SIPB(State Investment Promotion Board) in this regard shall be final and binding on all concerned stakeholders. The constitution of the State Investment

Promotion Board is as per Section 4 of the Bihar Industrial Investment Promotion Act, 2016.

- d. The implementation of various provisions of this Policy, including incentives, concessions, and facilitation measures, shall be subject to the issuance of detailed Operational Guidelines and/or statutory notifications, wherever required, by the Department of Industries, Government of Bihar.
- e. **Stability Guarantee:** Incentives sanctioned under this Policy shall remain protected and shall be applicable for the entire approved tenure, subject to compliance with the terms and conditions of the Policy.

## 2. VISION, MISSION & OBJECTIVES

### 2.1. Bihar Semiconductor Policy Vision

*"To establish Bihar as the Eastern India Tech Hub by 2030, creating a globally competitive ecosystem for semiconductor design, fabrication, assembly, testing, and packaging, while generating inclusive economic growth and high-quality employment for Bihar's youth."*

### 2.2. Mission

Five-Point Mission:

1. **Investment Attraction:** Secure committed investments of ₹25,000 Crores in semiconductor sector over policy period
2. **Employment Generation:** Create 2,00,000+ direct and indirect jobs in high-value semiconductor and ancillary industries
3. **Capacity Building:** Develop a skilled workforce of 50,000+ semiconductor professionals through dedicated training programs and academic partnerships
4. **Innovation Ecosystem:** Foster R&D and innovation by Start-ups through Semiconductor Research Center at IIT Patna.
5. **Semiconductor Ecosystem Development:** Develop an integrated semiconductor ecosystem encompassing manufacturing, design, equipment, ancillaries, testing, and supply-chain linkages to ensure long-term sustainability of the sector.

### 2.3. OBJECTIVES

#### 2.3.1 Economic Objectives

- Position Bihar among preferred semiconductor manufacturing destination in India
- Achieve semiconductor sector contribution of 5% to Bihar's GSDP by 2030

- Attract 3 major fab/ATMP units with investment >₹5,000 Crores each

### **2.3.2 Technological Objectives**

- Establish indigenous semiconductor fabrication capabilities
- Develop advanced packaging technologies
- Create compound semiconductor manufacturing ecosystem
- Foster semiconductor design capabilities with 10+ design houses
- Build testing and reliability centers meeting international standards

### **2.3.3 Social Objectives**

- Provide high-quality employment
- Promote local employment in Semiconductor units
- Promote women's participation
- Create skill development pathways from ITIs to advanced engineering
- Establish Bihar as a knowledge economy destination

### **2.3.4 Environmental Objectives**

- Ensure green semiconductor manufacturing with minimal environmental impact
- Promote water recycling
- Encourage renewable energy usage

## **3. DEFINITIONS**

For the purpose of this policy, the following definitions shall apply:

- a. Semiconductor Fab (Fabrication Unit): A facility engaged in the fabrication of semiconductor wafers using photolithography, etching, deposition, and other processes to create integrated circuits.
- b. ATMP (Assembly, Testing, Marking, and Packaging): A facility that assembles, tests, marks, and packages semiconductors die into final products.
- c. OSAT (Outsourced Semiconductor Assembly and Test): Third-party service providers offering ATMP services to fabless and integrated device manufacturers.
- d. Compound Semiconductors: Semiconductor materials composed of two or more elements, including but not limited to Gallium Nitride (GaN), Silicon Carbide (SiC), Gallium Arsenide (GaAs), and Indium Phosphide (InP).
- e. Display Fab: Manufacturing facility for display panels including LCD, OLED, AMOLED, Mini-LED, and Micro-LED displays.
- f. Semiconductor Design Units: Company engaged in designing integrated circuits, developing intellectual property (IP) cores, and providing design services.

- g. Technology Node: A technology node in semiconductors is a generation of chip manufacturing, defined by the size and complexity of transistors and other components. Smaller nodes represent more advanced technology.
- h. Approved Project Cost: For the purpose of calculation of incentives other than the capital subsidy under this policy, the Approved Project Cost shall mean the project cost finally approved by the SIPB as per the Bihar Industrial Investment Promotion Policy 2016. The Approved Project Cost shall be the basis for determining the incentives.
- i. Fixed Capital Investment: Fixed Capital Investment includes investment in land (maximum 20% of fixed capital investment excluding land), plant & machinery, electrical installations and all other assets necessary to set up the unit but shall exclude cost of working capital, contingencies, interest during construction period, and any other unexplained cost component
- j. Bihar Semiconductor Mission (BSM): The apex body constituted under this policy for governance, implementation, and monitoring.
- k. Date of Production (DOP): The "Date of Production" of an industrial unit shall mean the date on which the unit actually commences commercial production of the item for which the unit has been registered. As regards the date of production of Micro, Small and Medium units, the certificate issued by the respective General Manager, District Industries Centre (DIC) or Managing Director, Bihar Industrial Area Development Authority would be valid. For large industries, the certificate issued by Director MSME will be acceptable. In case of any dispute regarding the date of production, the decision of the Secretary/ Principal Secretary/ Additional Chief Secretary, Department of Industries shall be final.
- l. Ultra-Pure Water (UPW): Water with resistivity of 18.2 MΩ.cm at 25°C, required for semiconductor manufacturing processes.

Definitions not specifically mentioned here would carry the same meaning as in the Bihar Industrial Promotion Policy 2016

#### **4. General Eligibility Guidelines**

- Incentives under this policy would be applicable only for the investments made within the State.

- A project by any company/consortia/joint venture which has been qualified under any of the following schemes of India Semiconductor Mission of Government of India shall be eligible under this policy.
  - a. Scheme for setting up of Semiconductor Fabs in India
  - b. Scheme for setting up of Display Fabs in India
  - c. Scheme for setting up of Compound Semiconductors/ Silicon Photonics Sensors Fab and Semiconductor Assembly, Testing, Marking and Packaging (ATMP) OSAT facilities in India
  - d. Any other such scheme of the Government of India.
- Any unit manufacturing any product, wherein the manufacturing activity does not contribute to value addition shall not be considered under this policy. Units involved only in trading activities will not come under the purview of this policy.
- Units promoted by individuals/ firms/ companies etc. that at any point in the past were blacklisted by the government (state or central) will not be eligible to avail the benefits of this policy.
- Units promoted by individuals/ firms/ companies etc. that at any point in the past has defaulted on any loan availed from any bank or Financial Institutions (FIs) or has any dues payable to the government will not be eligible to avail the benefits of this policy.
- The policy may be amended and modified during implementation. However, all such amendments and modifications shall be applied prospectively and shall not curtail any benefit or concessions already granted under the policy.
- The unit/s must have GST registration of Bihar post notification of this policy.

#### **5. Fiscal Incentives for the Semiconductor Fab Units, Display Fab Units, Compound Semiconductor Units and ATMP/OSAT Units**

Any Semiconductor Fab Units, Display Fab Units, Compound Semiconductor Units and ATMP/OSAT Units by any company/consortia/joint venture which has been qualified under any schemes of Government of India shall be eligible to avail the benefits under this policy.

##### **5.1. Capital Subsidy**

The Eligible Units will be offered a Capital Subsidy by the Bihar Government as follows:

S.No	Type of units	Capital subsidy
1	Semiconductor Fabrication Units	60% of the GoI approved assistance.
2	Display Fabrication Units	60% of the GoI approved assistance.
3	Compound Semiconductors/ Silicon Photonics/ Sensors Fab/ Discrete Semiconductors Fab and Semiconductor Assembly Testing / Marking & Packaging (ATMP)/ Outsourced Semiconductors Assembly & Test (OSAT) facilities	60% of Capital Expenditure support provided by GoI.

The fiscal support will be released subject to the GoI's portion of fiscal support released, and in 'Pari Passu' of the same.

## 5.2. Land-related incentives

5.2.1. For every ₹100 crore of approved project cost, the Bihar Government through Bihar Industrial Area Development Authority (BIADA) shall provide **1 acre of land at a token amount of ₹1**, subject to availability and applicable terms and conditions as per BIADA land allotment policy. Furthermore, additional land required for the project shall be provided at 50% of the BIADA scheduled rate.

5.2.2. The exemption from Stamp Duty/ Registration Fee and Land Conversion to the approved projects/units under this policy shall be as per Bihar Industrial Investment Promotion Policy-2016

<b>Exemption from Stamp Duty/ Registration Fee</b>	<p>a. No stamp duty to be paid in respect of land allotted by the government to IDA/BIADA.</p> <p>b. 100% exemption of stamp duty/registration fees levied on lease/ sale /transfer of industrial land/shed as also those outside the jurisdiction of Industrial Area Development Authority would be available to all the new units. This exemption of stamp duty and registration fees will be granted only for the first time and will not be applicable in subsequent stages of lease/sale/transfer. This incentive will be available to new units only.</p> <p>c. 100% exemption of Stamp duty for Lease of Land/Shed/ Buildings and also mortgages and hypothecations deeds.</p> <p>d. The area of land required by the unit shall be fully</p>
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	described in the DPR and/or the Bank Appraisal Report prepared by the bank or financial institution which is supposed to extend the term loan to the unit.
Land Conversion Fees	100% exemption of "land conversion fees"/"change in land use" fees being levied for conversion of agricultural land.

### 5.3. Power tariff Subsidy

To ensure globally competitive operating costs for semiconductor manufacturing units, the eligible units can opt for either of following annual power tariff subsidy:

5.3.1. Special Power tariff of ₹5/unit. or

5.3.2. To promote sustainable and green industrial operations, eligible units shall be permitted to procure renewable energy through Open Access mechanisms. A tolerance limit of up to 20% shall be allowed for procurement from non-renewable energy sources. In such cases, the State Government shall reimburse 100% of applicable Cross Subsidy Surcharge (CSS) and wheeling charges, subject to compliance with applicable laws, regulations, and guidelines issued by the competent authorities.

The provisions of this policy relating to renewable energy procurement through Open Access shall be governed by The Electricity Act, 2003 of GoI and the Electricity (Promoting Renewable Energy Through Green Energy Open Access) Rules, 2022 of GoI, as amended from time to time.

The power tariff subsidy shall be provided for a period of 10 Years from the Date of Production to the eligible units.

In addition, the State shall extend the following infrastructure support for Eligible projects/units:

- **Power Infrastructure Subsidy:** Under the Power Infrastructure Subsidy provisions of this policy, eligible units shall be provided with a capital subsidy of up to 50 percent of the actual cost incurred on development of power infrastructure, as per the power requirement approved under the approved project cost.
- **Captive Renewable Energy Systems:** To promote energy security and sustainability, eligible units shall be provided with a capital subsidy of

50% of approved project cost on captive solar power installations, subject to a maximum limit of ₹50 crore.

- However, No unit shall be eligible to claim incentives for the same component or expenditure under more than one scheme. Where an investment component has been considered for incentive computation under any Government of India scheme, such component shall not be eligible for capital subsidy or any other incentive under the Bihar Semiconductor Policy, 2026. For example, if investment in power grid infrastructure and captive renewable energy systems is included as part of the project cost for availing capital subsidy from the Government of India, the unit shall not be permitted to claim incentives for the same components under this Policy.

#### **5.4. Water related Subsidy**

The State Government shall ensure and facilitate the availability of uninterrupted and adequate potable water supply to eligible Semiconductor projects at their respective project sites. In addition, the following water-related incentives shall be extended to eligible units:

- **Raw Water Supply at Concessional Rate:**

Raw water shall be supplied at a subsidized rate of ₹4 per cubic meter for a period of 10 years from the Date of Production.

- **Water Treatment Infrastructure Support:**

Eligible units shall be provided with a capital subsidy of 50% on the cost of setting up water treatment and management infrastructure, subject to a maximum limit of ₹50 crore per unit, to facilitate the establishment of high-quality, sustainable, and fab-grade water management systems.

#### **5.5. Interest Subsidy**

Eligible Semiconductor units shall be provided with an interest subsidy at the rate of 5% per annum upto Rs 25Cr/year for seven years from Date of Production on term loans availed from scheduled banks and/or financial institutions.

#### **5.6. Net SGST Reimbursement on Sale of Finished Products**

- Eligible Semiconductor units will be entitled to receive 100% reimbursement against the admitted SGST deposited in the account

of the state government (excluding any tax paid by them arising out of a pure trading business) from the date of Production.

- The reimbursement will be to a maximum ceiling of 100% of the approved project cost.
- The SGST reimbursement will be applicable only to the net tax payable, after adjustment of IGST and SGST credit available in the Electronic Credit Ledger, from the date of implementation of this policy. This reimbursement will be payable to eligible units up to 10 years from the date of production of the units as per terms & conditions of the BIIPP-2025

### **5.7. Semiconductor Research, Innovation & IP Promotion**

#### **a. R&D center at Academic Institution**

Department of Information Technology has set up Semiconductor R&D center with Clean Room facility at IIT Patna for supporting semiconductor research and innovation by Start-ups.

#### **b. Patent Filing Incentives**

To encourage creation and protection of intellectual property in the semiconductor sector, the State Government shall extend the following patent-related incentives to eligible units:

##### **Indian Patents:**

- Reimbursement of 75% of the costs incurred towards patent filing, prosecution, and maintenance in India.
- Subject to a cumulative ceiling of ₹10 lakh per unit; and
- Applicable to all semiconductor-related patents filed by the eligible unit.

##### **International Patents:**

- Reimbursement of 75% of the costs incurred towards patent filing and prosecution for overseas patent applications.
- Subject to a cumulative ceiling of ₹20lakhs per unit; and
- Applicable to patent applications filed in key global jurisdictions, including the United States, European Union, Japan, and China.

### **5.8. Skill Development & Training Support**

With a view to promoting local employment and development of a skilled workforce in the semiconductor sector, the State Government shall provide **100% reimbursement of eligible training costs** incurred by eligible units. **Such**

reimbursement shall be admissible only in respect of employees who are domiciled in the State of Bihar, subject to fulfillment of such conditions, documentation, and verification requirements as may be prescribed by the State Government from time to time.

- Financial Limit: Reimbursement shall be limited to ₹20 thousand per employee (**who are domiciled in the State of Bihar**).
- Coverage: The incentive shall be applicable for the first 1,000 employees.

## 6. Fiscal incentives for Semiconductor Design Units

Eligibility criteria for setting-up units for semiconductor design for Integrated Circuits (ICs), Chipsets, System on Chips (SoCs), Systems & IP Cores and semiconductor linked design units:

- The minimum Fixed Capital Investment should be 5 Cr.
- A project/unit by any company/consortia/joint venture which has been qualified under Semiconductor Design linked Incentive scheme of MeitY, Government of India or any such scheme of the MeitY, Gol and any amendments thereof, shall be eligible under this policy.

### 6.1. Product Design Linked Incentive

Eligible units engaged in semiconductor design activities, including Integrated Circuits (ICs), Chipsets, Systems-on-Chip (SoCs), Systems & IP Cores, and other semiconductor-linked design facilities, shall be provided an additional reimbursement of 20% of eligible expenditure as approved by the Government of India, subject to a maximum incentive of ₹15 crore per unit, by the Government of Bihar.

Further, where a unit employs more than 70% of its total workforce possessing permanent domicile of Bihar, such unit shall be eligible for an additional incentive of 5% of all the eligible expenditure provided by Govt. of India to a maximum of 5 Cr.

### 6.2. Lease Rental Subsidy

Eligible units operating from leased office/commercial space shall be reimbursed with the Lease Rental incentive of 50% of the Lease rental amount paid and will be provided for a period of five (5) years, subject to following conditions:

- a. The Office space should be located in the State of Bihar.
- b. Per employee office space should not be considered more than 120 sq. ft of super built-up area.

- c. For the purpose of this incentive calculation, lease rate shall be limited to INR 55 per sq. ft per month for the policy notification financial year and this limit shall be increased by INR 2.50 every financial year.

### 6.3. Power tariff Subsidy

Eligible Semiconductor Design units will be provided with an annual reimbursement of 25% of the Energy bill for a period of five (5) years from the Date of Production. The "Energy bill" in this case is comprised of the Fixed charges, Energy Charges and Electricity Duty excluding any applicable discount/late payment.

### 6.4. Patent Filing Incentives

To encourage creation and protection of intellectual property in the semiconductor sector, the State Government shall extend the following patent-related incentives to eligible units:

#### Indian Patents:

- Reimbursement of 75% of the costs incurred towards patent filing, prosecution, and maintenance in India;
- Subject to a cumulative ceiling of ₹10 lakh per unit; and
- Applicable to all semiconductor-related patents filed by the eligible unit.

#### International Patents:

- Reimbursement of 75% of the costs incurred towards patent filing and prosecution for overseas patent applications;
- Subject to a cumulative ceiling of ₹20 lakhs per unit; and  
Applicable to patent applications filed in key global jurisdictions, including the United States, European Union, Japan, and China.

### 6.5. Skill Development & Training Support

With a view to promoting local employment and development of a skilled workforce in the semiconductor sector, the State Government shall provide **100% reimbursement of eligible training costs** incurred by eligible units. **Such reimbursement shall be admissible only in respect of employees who are domiciled in the State of Bihar**, subject to fulfillment of such conditions, documentation, and verification requirements as may be prescribed by the State Government from time to time.

- Financial Limit: Reimbursement shall be limited to ₹20 thousand per employee (**who are domiciled in the State of Bihar**).
- Coverage: The incentive shall be applicable for the first 1,000 employees.

## **7. Fiscal incentives for Units approved under Electronic Component Manufacturing Scheme of Gol (Gov of India)**

Eligibility criteria for setting-up of Electronic Component Manufacturing Scheme unit in Bihar:

- i. The target segment, products covered under certain target segments, cumulative incremental investment threshold, Incremental sales threshold and cumulative incremental employment threshold and other eligibility criteria shall be as per the eligibility criteria defined by Electronic Component manufacturing scheme of MeitY, Gol notified on 8 Apr 2025 and amendment dated 7 July 2025 and thereof, as and when applicable.
- ii. A project by any company/consortia/joint venture which has been qualified under Electronic Component Manufacturing scheme of MeitY, Government of India shall be eligible under this policy.

### **7.1. Gol-linked incentive under Electronic Component Manufacturing Scheme**

Bihar Government will provide additional 20% of the incentive being provided by Govt. of India under Electronic Component Manufacturing Scheme notified by MeitY under any category.

Further, where a unit employs more than 70% of its total workforce possessing permanent domicile of Bihar, such unit shall be eligible for an enhanced incentive of additional 5% of all the incentive being provided by Govt. of India.

The fiscal support will be released subject to the GOI's portion of fiscal support being released by them, and in 'Pari Passu' of the same.

### **7.2. Land-related incentives**

For every ₹100 crore of approved project cost, the Bihar Government through Department of Industries or Bihar Industrial Area Development Authority (BIADA) shall provide **1 acre of land at a token amount of ₹1**, subject to availability and applicable terms and conditions.

Furthermore, additional land required for the project shall be provided at 50% of the BIADA scheduled rate.

7.2.1. The exemption from Stamp Duty/ Registration Fee and Land Conversion to the approved projects/units under this policy shall be as per Bihar Industrial Investment Promotion Policy-2016

<p><b>Exemption from Stamp Duty/ Registration Fee</b></p>	<p>a) No stamp duty to be paid in respect of land allotted by the government to IDA/BIADA.</p> <p>b) 100% exemption of stamp duty/registration fees levied on lease/ sale /transfer of industrial land/shed as also those outside the jurisdiction of Industrial Area Development Authority would be available to all the new units. This exemption of stamp duty and registration fees will be granted only for the first time and will not be applicable in subsequent stages of lease/sale/transfer. This incentive will be available to new units only.</p> <p>c) 100% exemption of Stamp duty for Lease of Land/Shed/ Buildings and also mortgages and hypothecations deeds.</p> <p>d) The area of land required by the unit shall be fully described in the DPR and/or the Bank Appraisal Report prepared by the bank or financial institution which is supposed to extend the term loan to the unit.</p>
<p>Land Conversion Fees</p>	<p>100% exemption of "land conversion fees"/"change in land use" fees being levied for conversion of agricultural land.</p>

8. **Customized Incentive:** Projects which involve an investment of more than ₹2000 crores in plant and machinery and provide direct employment to more than 1000 persons will give long term direct and indirect benefits to investment environment and employment generation in the state. Any such investment project will be entitled to a Customized Incentive Package (as per BIIPP 2025).

## 9. Policy Implementation & Monitoring

9.1. The Department of Industries, Govt. of Bihar will issue detailed implementation guidelines to achieve the goals and objectives of this Policy.

9.2. **Bihar Semiconductor Mission (BSM):** The apex body constituted under this policy for overall governance, implementation, coordination, and monitoring of the policy. The BSM shall oversee supervision of projects, track physical and

financial progress of units, facilitate inter-departmental coordination, and ensure timely resolution of issues to enable effective implementation of approved investments

The High-Level Empowered Committee under the Chairmanship of the Chief Secretary, Bihar is constituted as follows:

S. No	Members of the Committee	Designation
1	Chief Secretary, Bihar	Chairperson
2	Additional Chief Secretary/Principal Secretary/Secretary, Department of Industries	Member Secretary
3	Additional Chief Secretary/Principal Secretary/Secretary, Finance Department	Member
4	Additional Chief Secretary/ Principal Secretary/Secretary, Department of IT	Member
5	Additional Chief Secretary/ Principal Secretary / Secretary, Energy Department, Govt of Bihar	Member
6	Additional Chief Secretary/Principal Secretary / Secretary, Department of Water Resources Department, Govt of Bihar	Member

9.3. Dovetailing with Central Government policies and schemes: Under the Policy, dovetailing with Central Government policies and schemes would be allowed. Investors shall have to declare the type and quantum of incentives availed/ to be availed by them under the Central Government policies.

9.4. Stage of Project Clearance and Disbursement

9.4.1. Stage 1 Clearance: For availing incentive under this policy, the eligible units would be required to submit the application. The application should be submitted on the Single Window Clearance Portal ([swc2.bihar.gov.in](http://swc2.bihar.gov.in)).

9.4.2. Approval of Stage-1 clearance shall usually be granted within 15 business days of the submission of complete application along with the required supporting documents. The Maximum time limit for the State Investment Promotion Board (SIPB) approval shall be 30 days.

9.4.3. Financial Incentive Clearance:

- a. Entrepreneurs should submit an application for Financial Clearance on the Single Window Clearance portal (swc2.bihar.gov.in). Financial Clearance shall be granted usually within 15 business days of receipt of complete application with the required supporting documents. The maximum time limit for the State Investment Promotion Board (SIPB) approval shall be 30 days.
- b. Monitoring the policy implementation will be managed by the State Investment Promotion Board (SIPB). Quarterly reports shall be placed before SIPB regarding Implementation of this policy.
- c. All matters of interpretation/disputes shall be decided by the Secretary/ Principal Secretary/ Additional Chief Secretary, Department of Industries. Such interpretation/decision shall be final and binding for all concerned parties.

9.4.4. Financial Incentives Disbursement: The amount of Financial Incentives for disbursement shall be approved by Additional Chief Secretary (ACS) /Principal Secretary/Secretary, Department of Industries, Govt. of Bihar.

9.5. Applicant Units shall inform the SIPB, Department of Industries, GoB about commencement of Date of Production within 15 days. Department of Industries shall verify the same and issue Date of Production certificate. Verification shall also be done at time of disbursal and as and when required after commercial production/operation.

#### 9.6. Implementing Conditions

- The detailed terms and conditions under this policy have been published by the Department after consultations with the departments concerned and approval of the Departmental Minister.
- The Department of Industries, Government of Bihar shall implement this policy.
- Words used in this Policy shall have the same meaning as assigned to them under the Bihar Industrial Investment Promotion Policy, 2016, unless specified otherwise. All matters of interpretation/disputes shall be decided by the Secretary/ Principal Secretary/ Additional Chief Secretary/ Department of Industries. Such interpretation/decision shall be final and binding for all concerned parties.

- This policy shall come into **effect from the date of notification** and shall remain in **operation till the policy period validity**.
- 9.7. The applicant availing benefits under the Policy shall be subject to the conditions, procedures, instructions, clarifications or amendments issued, from time to time, for this policy and other applicable policies issued by Department of Industries, Government of Bihar.
- 9.8. If any false declaration is given for the purpose of availing incentives or if incentives are availed for a unit that was not eligible or any violation of condition of this policy, the amount of incentive is liable to be recovered from the date of availing such incentives along with interest compounded annually @ 18% per annum. In case of non-payment within the stipulated time, the State Government shall recover such amounts including interest as arrears of land revenue.
- 9.9. The incentives will cease either on the exhaustion of the applicable quantum or on the completion of the eligible period, whichever is earlier. Any unutilized incentive at the end of the eligibility period shall lapse.
- 9.10. In case of any discrepancy in the meaning and interpretation of the translated version of this policy, the English language version shall be binding in all respect and shall prevail.
10. Bihar Semiconductor Policy-2026 has been approved as item number 27 of the proceedings of the Council of Ministers meeting dated 29.01.2026.

By the order of the Governor of Bihar

  
(Kundan Kumar)

Secretary,

Department of Industries, Bihar, Patna.

Memo No- 153(E) /Patna,

Dated- 30/01/2026

File No- DMSME/Niti-Semiconductor/01/2026/Industry

Copy to: Principal Secretary to Chief Minister, Bihar/P.S. to Minister, Department of Industries/O.S.D to Chief Secretary, Bihar, Patna/Principal P.S. to the Development Commissioner, Bihar, Patna for information.

  
Secretary,

Department of Industries, Bihar, Patna.

Memo No- 153(E) /Patna, Dated- 30/04/2026  
File No- DMSME/Niti-Semiconductor/01/2026/Industry  
Copy to: The Accountant General (A&E), Bihar, Patna/ Treasury Officer, Secretariat  
Treasury, Vikas Bhawan, Patna for information.

  
Secretary,

Department of Industries, Bihar, Patna.

Memo No- 153(E) /Patna, Dated- 30/04/2026  
File No- DMSME/Niti-Semiconductor/01/2026/Industry  
Copy to: All Add. Chief Secretary/All Principal Secretary/All Secretary /All Heads of  
Departments, Bihar / P.S. to Secretary, Department of Industries, Bihar, Patna/  
Director of Industries, Bihar, Patna/ Director, MSME, Bihar, Patna/ Director, Food  
Processing/ Director, Handloom & Sericulture/ All Corporations and Authorities of  
the Department of Industries for information and necessary action.

  
Secretary,

Department of Industries, Bihar, Patna.

Memo No- 153(E) /Patna, Dated- 30/04/2026  
File No- DMSME/Niti-Semiconductor/01/2026/Industry  
Copy to: All Divisional Commissioners, Bihar/ Resident Commissioner, Bihar  
Bhawan, New Delhi/ All District officers, Bihar/Director, M.S.M.E.D.I, Patliputra,  
Patna/Muzaffarpur /All General Managers, District Industries Centres, Bihar for  
information and necessary action.

  
Secretary,

Department of Industries, Bihar, Patna.

Memo No- 153(E) /Patna, Dated- 30/04/2026  
File No- DMSME/Niti-Semiconductor/01/2026/Industry  
Copy to: The Officer-In-Charge, E-Gazette Cell, Finance Department, Bihar, Patna  
with a Soft Copy (CD) and 02 Hard Copy of Resolution to publish in the special  
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Secretary,

Department of Industries, Bihar, Patna.

Memo No- 153 (E) /Patna,

Dated- 30/01/2026

File No- DMSME/Niti-Semiconductor/01/2026/Industry

Copy to: IT Manager, Department of Industries, Bihar, Patna for uploading the copy of the resolution on the Departmental Website.

  
Secretary,

Department of Industries, Bihar, Patna..